

Docket No.: 43890-470



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Kunio KISHIMOTO, et al.

Serial No.: 09/736,499

Group Art Unit: 3724

Filed: December 15, 2000

Examiner: Timothy V. ELEY

For: METHOD OF MANUFACTURING A CIRCUIT BOARD AND ITS
MANUFACTURING APPARATUS

#5
Election
E. J. Fogarty
6/20/03

RESPONSE TO RESTRICTION REQUIREMENT

Mail Stop Restriction Requirement
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RECEIVED
JUN 19 2003
TECHNOLOGY CENTER R3700

Sir:

Noting the Office Action of May 21, 2003 wherein restriction has been required, Applicant(s) hereby elect Group I (claims 1-49, drawn to a method of manufacturing a circuit board) for prosecution in the above-identified application.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,
MCDERMOTT, WILL & EMERY

Michael E. Fogarty
Registration No. 36,139

600 13th Street, N.W.
Washington, DC 20005-3096
(202) 756-8000 MEF:men
Facsimile: (202) 756-8087
Date: June 17, 2003